



SHREEYASH PRATISHTHAN'S
SHREEYASH COLLEGE OF ENGINEERING & TECHNOLOGY

B.Tech. First Year Vacancy Position of SYCET as per STATE CET CELL
(Revised Schedule)

Sr. No.	Branch	Against CAP Vacancy	Institute Level
1	Computer Science and Engineering	16	08
2	Computer Science and Engineering (Data Science)	07	00
3	Electronics and Computer Engineering	01	00
4	Mechanical Engineering	09	00
5	Electrical Engineering	06	00
6	Civil Engineering	13	00
7	Computer Science and Engineering (AIML)	06	00
8	VLSI	16	00
9	5G	07	00
	Total	81	08

***Vacancy position is at the end of third round may change upto commencement of spot round due to cancellation of admission by the candidate.**

**** As spot round is dynamic round vacancy may change so all interested candidates are requested to be present till the end of spot round.**



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**Announcement Revised Schedule Activity of Spot Round / Institute Level on 23th
October 2024 for B.Tech First year programs at SYCET for the Academic Year
2024-25**

Shreeyash College of Engineering & Technology (SYCET) Aurangabad invites applications from eligible candidates for the Institute Level Revise Spot Round for the First Year of the Four Year Degree Courses in Engineering/Technology for the academic year 2024-25. The round will be conducted on Wednesday, 23th October 2024 at the SYCET Campus (as per rule No 13 mentioned in Information Brochure for academic year 2024-25 published by State Common Entrance Test Cell, Maharashtra State.)

*Rules, regulations prescribed by state CET CELL will be in effect.

Please go through the following instructions carefully before beginning with the process of application.

1. Eligibility: 1.1 Only those candidates, who have registered through CET portal and verified their applications by FC, are eligible to apply the Institute Level Spot Round.

1.2 The Provisional vacant seat matrix for the Institute Level Spot Round will be published on Tuesday 1st October 2024 (Final vacancies will be published on 23/10/2024 at 10.00am i.e. on the day of spot round)

1.3 Institute level spot round will be dynamic in nature. Hence there is a possibility of addition of seats due to upgrade/change in branch by candidates during admission process. The vacancy/vacancies generated due to upgrade /Change of branch during admission process will be offered to the next meritorious candidate.

Spot Round Schedule Activity

Sr. No.	Activity	Schedule	
		First Date	Last Date
1.	Vacancy position available on SYCET Website	01-10-2024 up to 5.00PM	
2.	Application invited for all Registered Candidates in the order of Final General Merit Number Subject to availability of vacancy as per the schedule published on institute website.	01/10/2024 to 22/10/2024 up to 5.00PM	
3.	Display of the Provisional Merit list on Notice Board	23/10/2024 10.00 AM	
4.	Display of the Final Merit list on Notice Board	23/10/2024 11.00 PM	
5.	Confirmation of spot admission to allotted seats to Institute	23/10/2024 12.00 PM onwards	